

FIG. 1.

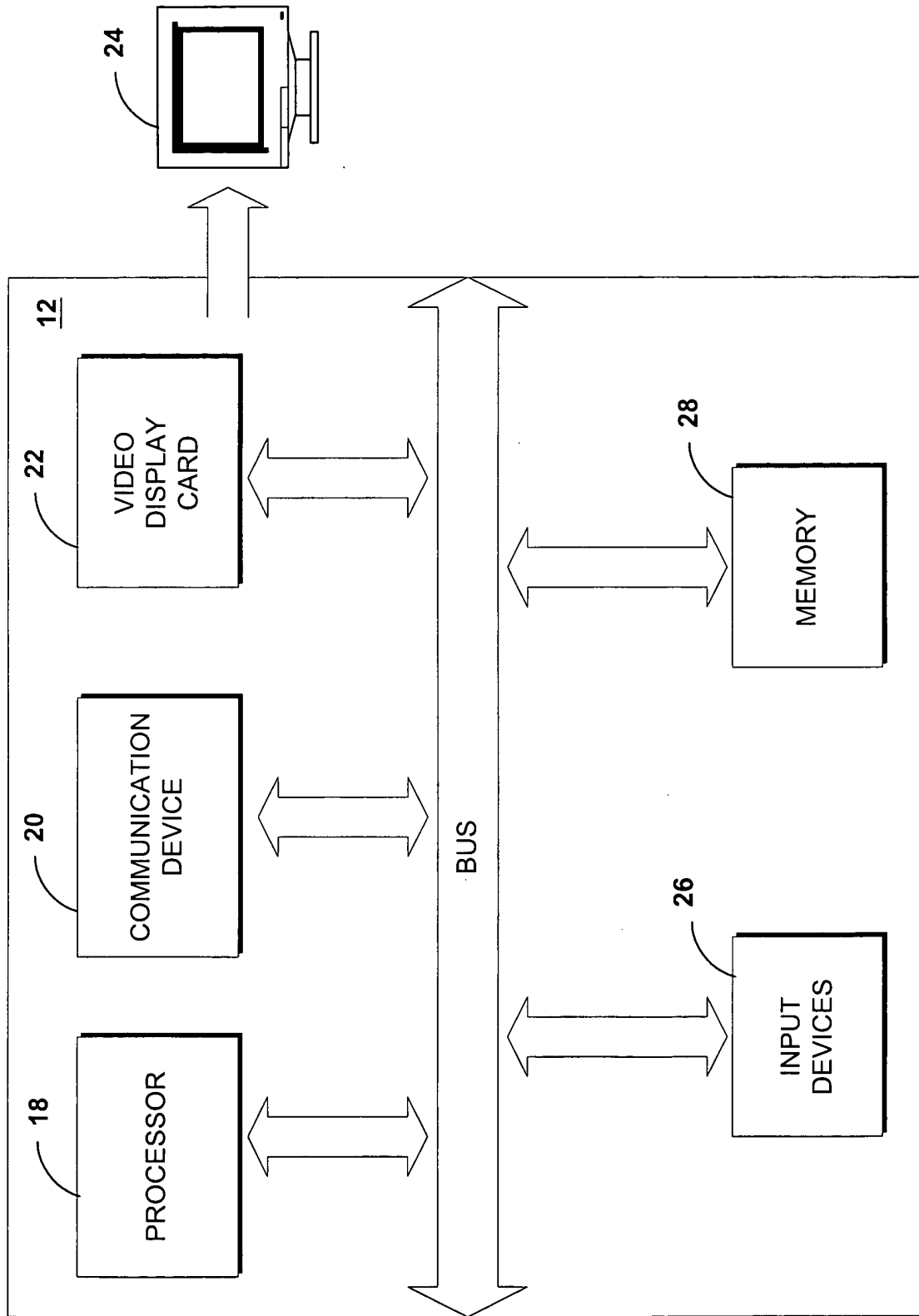


FIG. 2.

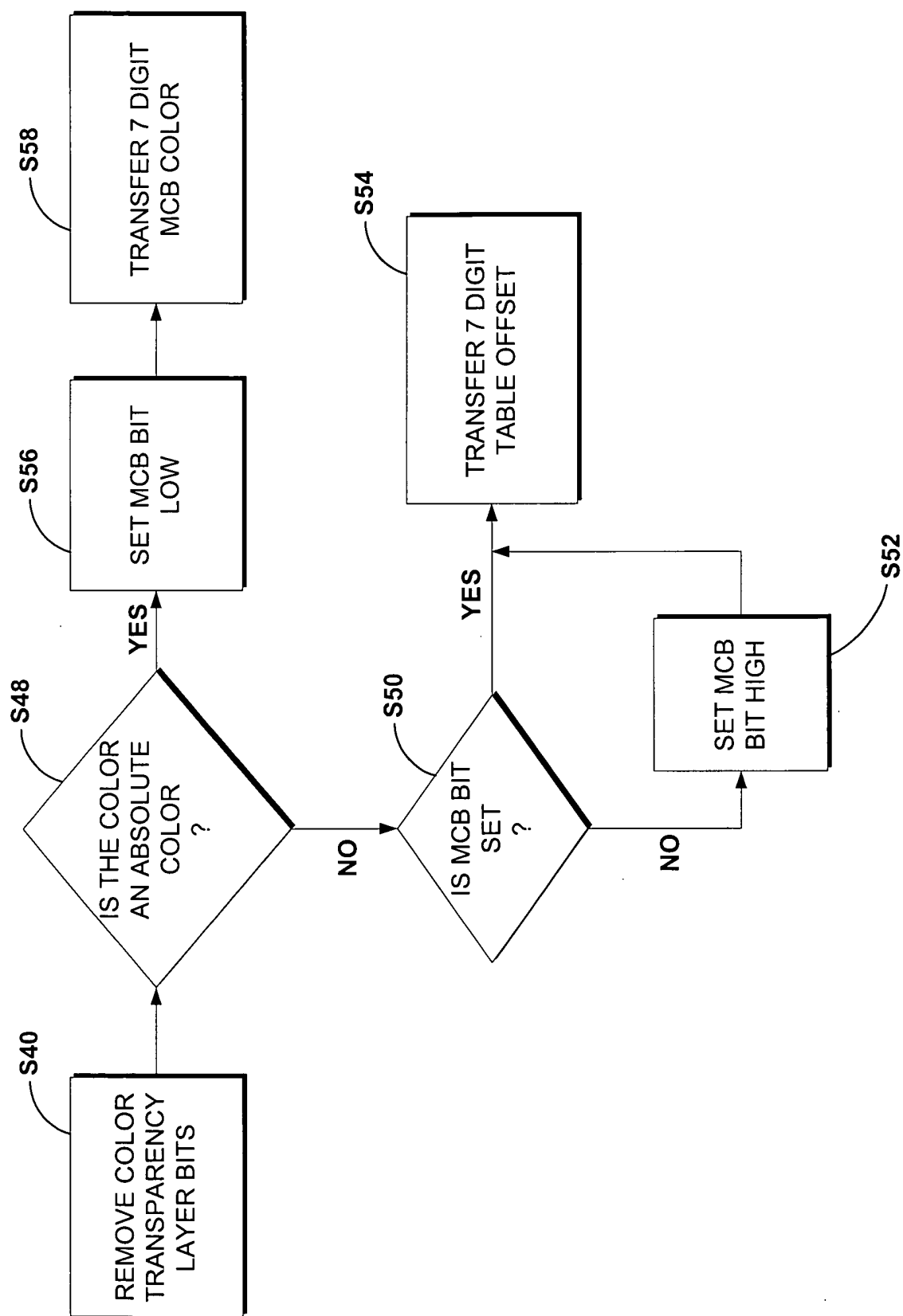


FIG. 4.

FIG. 5 is a block diagram of a system 400. The system 400 includes a processor 402, a memory 404, a network interface 406, and a user interface 408. The processor 402 is connected to the memory 404, the network interface 406, and the user interface 408. The memory 404 stores data and instructions. The network interface 406 enables communication with a network. The user interface 408 provides input and output for a user.

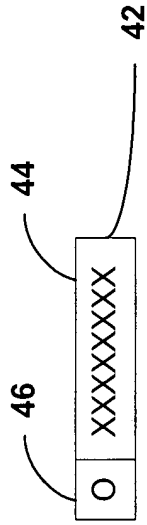


FIG. 5.

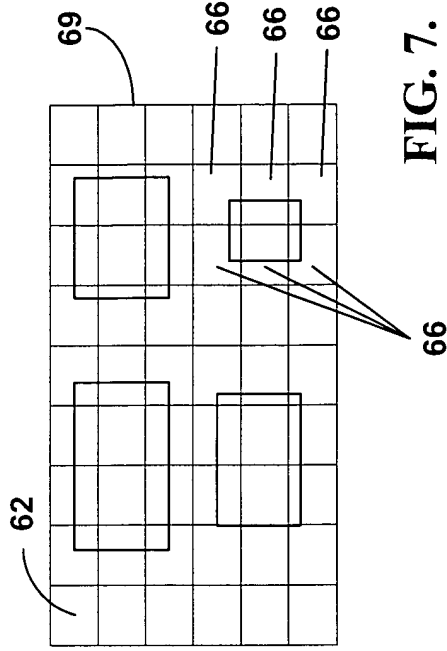
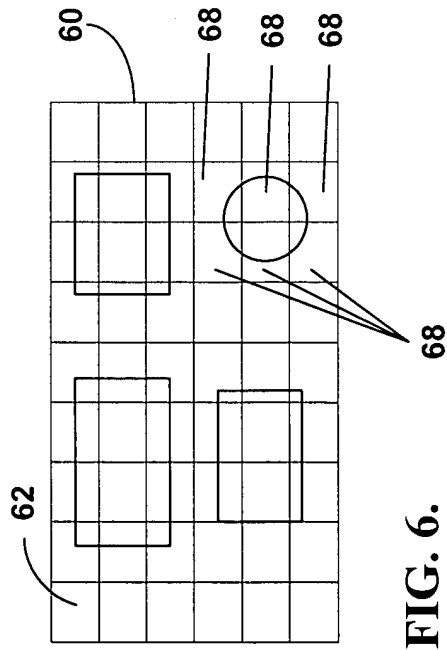


FIG. 8.

AB	CD	EF	GH	IJ	KL	MN	OP	QR
ST	UV	WX	YZ	12	34	56	78	90
00	01	02	03	04	05	06	07	08
09	10	11	12	13	14	15	16	17
18	19	20	21	22	23	24	25	26
27	28	29	30	31	32	33	34	35

FIG. 9.

AB	CD	EF	GH	IJ	KL	MN	OP	QQ
ST	UV	WX	YZ	11	34	56	78	90
00	01	02	03	04	05	06	07	08
09	10	11	12	13	14	15	16	18
18	19	20	21	22	23	24	25	26
27	28	33	30	31	32	33	34	35

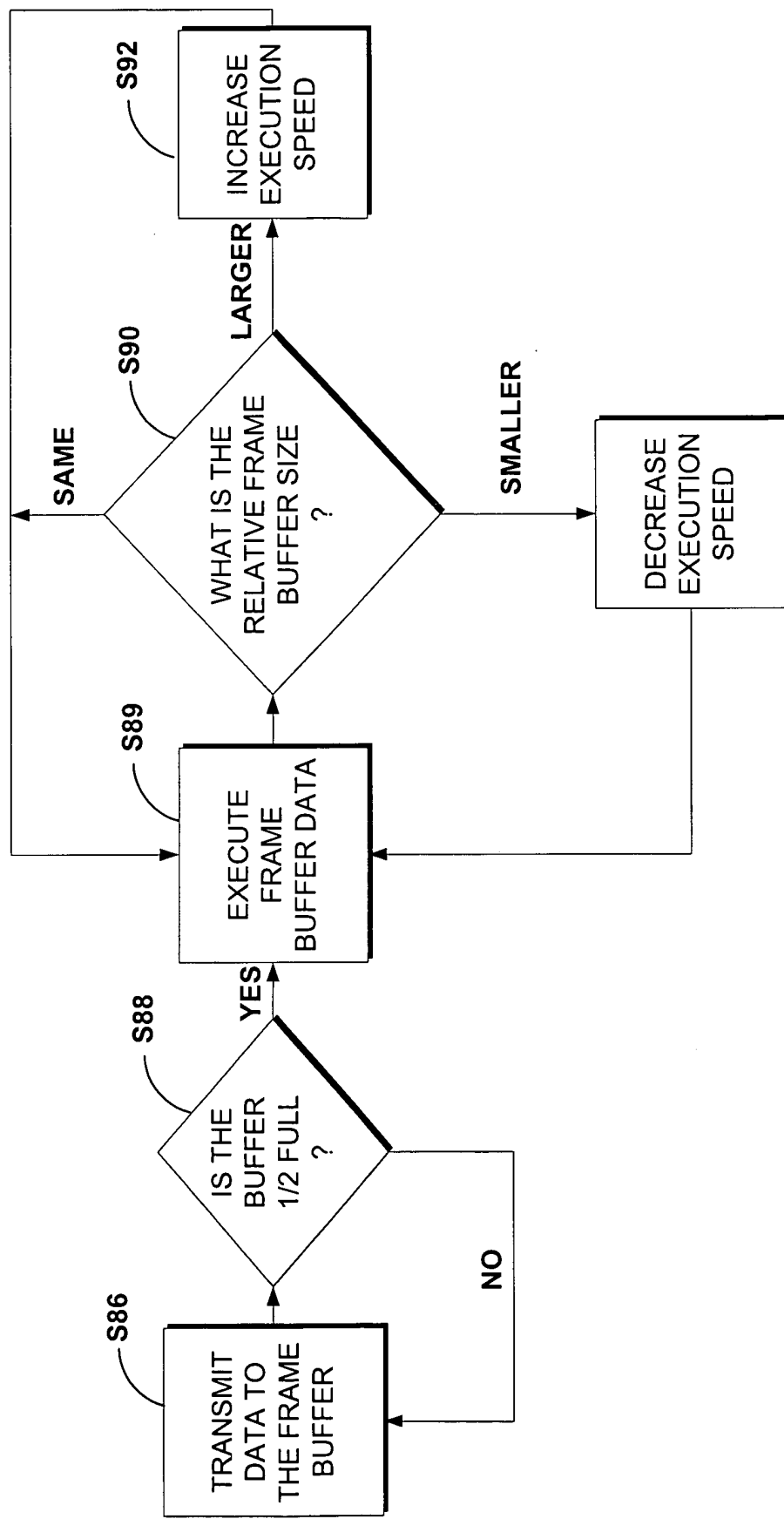


FIG. 10.

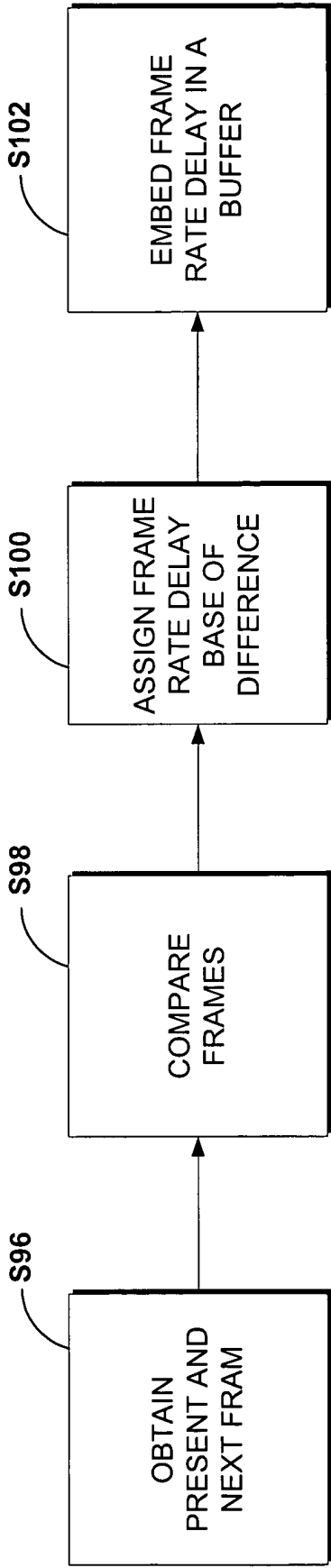


FIG. 11.

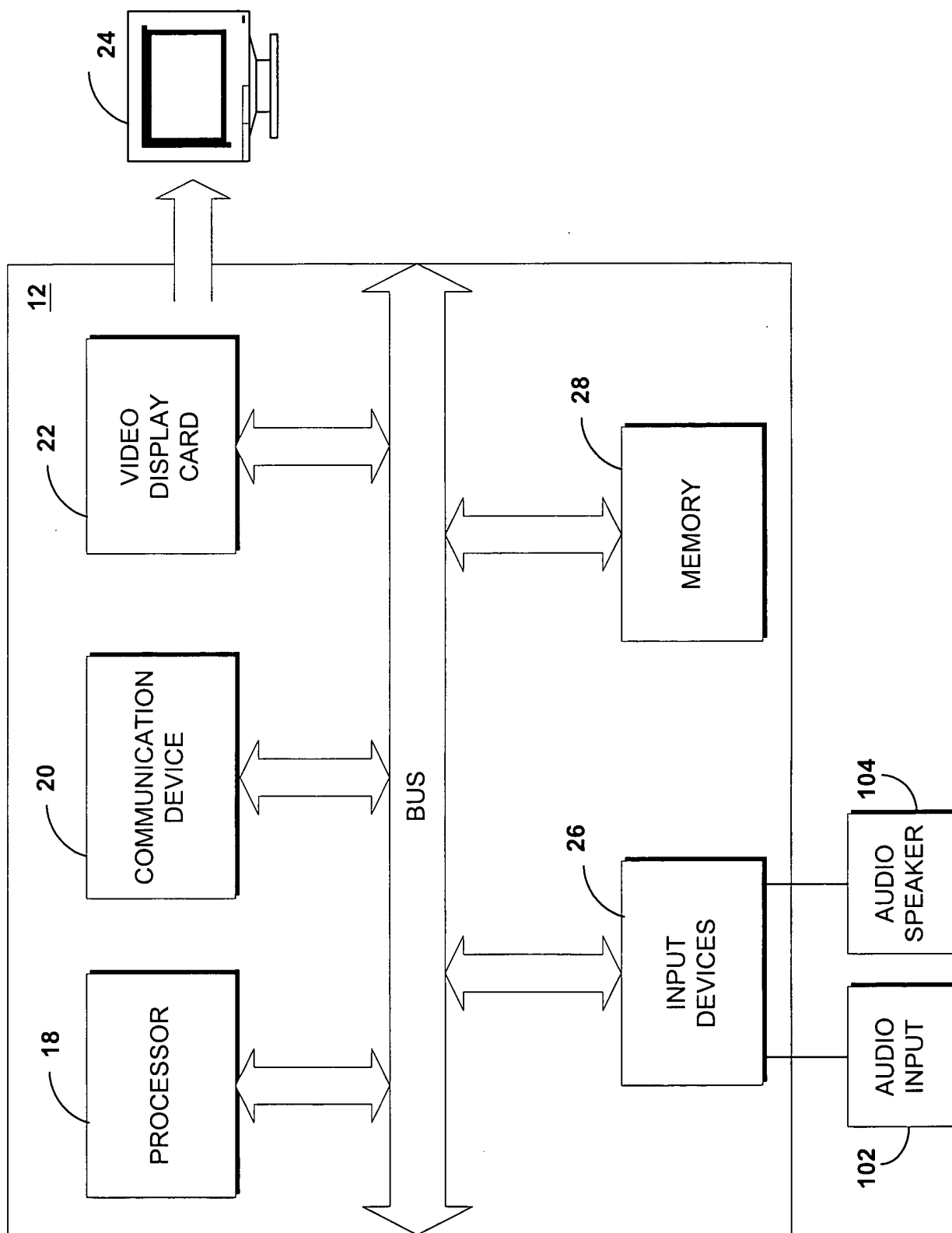


FIG. 12.